



09/07/99

LAW OFFICES

ANTONELLI, TERRY, STOUT & KRAUS, LLP

SUITE 1800

1300 NORTH SEVENTEENTH STREET
ARLINGTON, VIRGINIA 22209DONALD R. ANTONELLI
DAVID T. TERRY
MELVIN KRAUS
WILLIAM I. SOLOMON
GREGORY E. MONTONE
RONALD J. SHORE
DONALD E. STOUT
ALAN E. SCHIAVELLI
JAMES N. DRESSER
CARL I. BRUNDIDGE
PAUL J. SKWIERAWSKIRANDALL S. SVIHLA
DAVID S. LEE
ROBERT M. BAUER
DEMETRA J. MILLS
HUNG H. BUIOF COUNSEL
DALE CURTIS HOGUE, SR.
CHITTARANJAN N. NIRMEL, PHDPATENT AGENT
LARRY N. ANAGNOSTELEPHONE
(703) 312-6600FACSIMILE
(703) 312-6600E-MAIL
email@antonelli.com

September 7, 1999

*ADMITTED OTHER THAN VA.

Honorable Commissioner of
Patents and Trademarks
Washington, D.C. 20231Attorney Docket Number: 520.37546X00
Customer Number (020457)

Sir:

Attached please find the application papers of Kazuyoshi TORII, Hiroshi MIKI, Yoshihisa FUJISAKI, covering new and useful improvements in SEMICONDUCTOR DEVICE AND ITS FABRICATION METHOD, comprising:

Specification, Twenty-Eight (28) Claims and Abstract of the Disclosure (35 pages)

English language, Combined Declaration and Power of Attorney (2 pages)

Fifteen (15) Sheets of Drawings Showing Figures 1-26

Assignment and Recording of Assignment Letter

U.S. Government Filing Fee of \$1,668.00

U.S. Government Recording Fee of \$40.00

Change of Correspondence Address

Letter Claiming Right of Priority and Certified Copy of Japanese Patent Application No. 10-256287

Information Disclosure Statement Under 37 CFR 1.97 & 1.98 With Attached References

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$\{x_{i,j}^{(t)}\}$ and $\{y_{i,j}^{(t)}\}$ will form a $\{x_{i,j}^{(t)}\}$ and $\{y_{i,j}^{(t)}\}$ in \mathcal{X} and \mathcal{Y} respectively.

ANTONELLI, TERRY, STOUT & KRAUS, LLP

GEM/nac
Attachments